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Atty. Docket No.
2000.050800Serial No.
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List of Patents and Publications for Applicant's

Applicant
JOE A. RICKS

INFORMATION DISCLOSURE STATEMENT

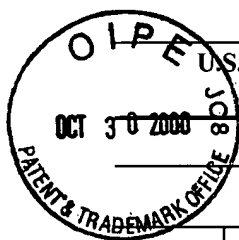
(Use several sheets if necessary)

Filing Date:
July 11, 2000Group:
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U.S. Patent Documents

Foreign Patent Documents

Other Art



U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
	A1						
	A2						
	A3						
	A4						
	A5						

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						
	B2						

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
RR	C1	Intel Corporation, ATX Specification Version 2.01, pp. 1-27; February, 1997
RR	C2	Intel Corporation, ATX Specification Version 2.03, pp. 1-30; December, 1998
RR	C3	Fairchild Semiconductor, N2N7000/2N7002/NDS7002A N-Channel Enhancement Mode Field Effect Transistor, 14 pp. (unnumbered) November, 1995
RR	C4	Intel Microsoft Toshiba, Advanced Configuration and Power Interface Specification, Rev. 1.0b pp. 1-323 February 2, 1999
RR	C5	Intel Microsoft Toshiba, Advanced Configuration and Power Interface Specification, Rev. 2.0 pp. 1-467 July 27, 2000
	C6	
	C7	
	C8	

EXAMINER:

DATE CONSIDERED:

9/7/02

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